<IGBT Modules>

CM100DY-13T

HIGH POWER SWITCHING USE
INSULATED TYPE

dual switch (half-bridge)

- Collector current I_C .... **100** A
- Collector-emitter voltage V_{CES} .... **650** V
- Maximum junction temperature T_{vjmax} .... **175** °C

- ●dual switch (half-bridge)
- ●Copper base plate (Nickel-plating)
- ●Tin-plating tab terminals
- ●RoHS Directive compliant
- ●UL Recognized under UL1557, File No. E323585

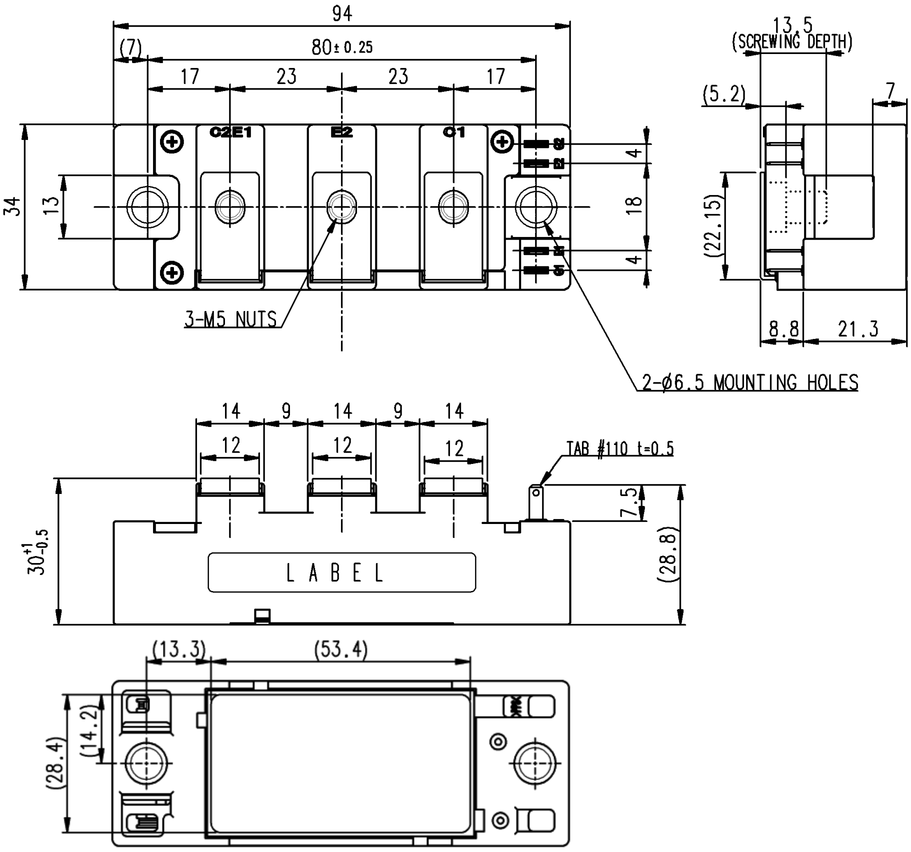
APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.

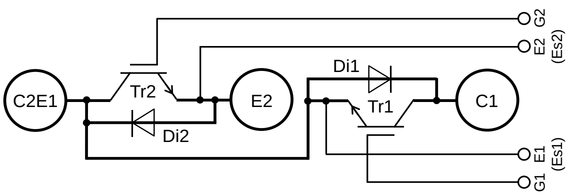
OPTION (Below options are available.)

●PC-TIM (Phase Change Thermal Interface Material) pre-apply

OUTLINE DRAWING & INTERNAL CONNECTION **Dimension in mm**



INTERNAL CONNECTION



Tolerance otherwise specified

Division of Dimension			Tolerance
0.5	to	3	±0.2
over 3	to	6	±0.3
over 6	to	30	±0.5
over 30	to 120		±0.8
over 120	to 400		±1.2

JIS B 0405 c

MAXIMUM RATINGS (T_{vj}=25 °C, unless otherwise specified)

Symbol	Item	Conditions	Rating	Unit
V_{CES}	Collector-emitter voltage	G-E short-circuited	650	V
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
I_C	Collector current	DC, T_C=125 °C (Note2, 4)	100	A
I_{CRM}		Pulse, Repetitive (Note3)	200	
P_{tot}	Total power dissipation	T_C=25 °C (Note2, 4)	775	W
I_E (Note1)	Emitter current	DC (Note2)	100	A
I_{ERM} (Note1)		Pulse, Repetitive (Note3)	200	
V_{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T_{vjmax}	Maximum junction temperature	Instantaneous event (overload)	175	°C
T_{Cmax}	Maximum case temperature	(Note4)	125	
T_{vjop}	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T_{stg}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS (T_{vj}=25 °C, unless otherwise specified)

Symbol	Item	Conditions		Limits			Unit
				Min.	Typ.	Max.	
I_{CES}	Collector-emitter cut-off current	V_{CE}=V_{CES}, G-E short-circuited		-	-	1.0	mA
I_{GES}	Gate-emitter leakage current	V_{GE}=V_{GES}, C-E short-circuited		-	-	0.5	μA
$V_{GE(th)}$	Gate-emitter threshold voltage	I_C=10 mA, V_{CE}=10 V		5.4	6.0	6.6	V
V_{CEsat} (Terminal)	Collector-emitter saturation voltage	I_C=100 A, V_{GE}=15 V,	T_{vj}=25 °C	-	1.35	1.65	V
		Refer to the figure of test circuit	T_{vj}=125 °C	-	1.45	-	
		(Note5)	T_{vj}=150 °C	-	1.50	-	
V_{CEsat} (Chip)		I_C=100 A,	T_{vj}=25 °C	-	1.30	1.55	V
		V_{GE}=15 V,	T_{vj}=125 °C	-	1.35	-	
		(Note5)	T_{vj}=150 °C	-	1.35	-	
C_{ies}	Input capacitance	V_{CE}=10 V, G-E short-circuited		-	-	13.4	nF
C_{oes}	Output capacitance			-	-	0.6	
C_{res}	Reverse transfer capacitance			-	-	0.3	
Q_G	Gate charge	V_{CC}=300 V, I_C=100 A, V_{GE}=15 V		-	0.41	-	μC
$t_{d(on)}$	Turn-on delay time	V_{CC}=300 V, I_C=100 A, V_{GE}=±15 V,		-	-	200	ns
t_r	Rise time			-	-	150	
$t_{d(off)}$	Turn-off delay time	R_G=6.2 Ω, Inductive load		-	-	400	
t_f	Fall time			-	-	400	
V_{EC} (Note.1) (Terminal)	Emitter-collector voltage	I_E=100 A, G-E short-circuited,	T_{vj}=25 °C	-	2.05	2.85	V
		Refer to the figure of test circuit	T_{vj}=125 °C	-	1.95	-	
		(Note5)	T_{vj}=150 °C	-	1.95	-	
V_{EC} (Note.1) (Chip)		I_E=100 A,	T_{vj}=25 °C	-	1.90	2.65	V
		G-E short-circuited,	T_{vj}=125 °C	-	1.80	-	
		(Note5)	T_{vj}=150 °C	-	1.80	-	
t_{rr} (Note1)	Reverse recovery time	V_{CC}=300 V, I_E=100 A, V_{GE}=±15 V,		-	-	150	ns
Q_{rr} (Note1)	Reverse recovery charge	R_G=6.2 Ω, Inductive load		-	3.5	-	μC
E_{on}	Turn-on switching energy per pulse	V_{CC}=300 V, I_C=I_E=100 A,		-	1.2	-	mJ
E_{off}	Turn-off switching energy per pulse	V_{GE}=±15 V, R_G=6.2 Ω, T_{vj}=150 °C,		-	5.1	-	
E_{rr} (Note1)	Reverse recovery energy per pulse	Inductive load		-	1.8	-	mJ
$R_{CC'+EE'}$	Internal lead resistance	Main terminals-chip, per switch, T_C=25 °C (Note4)		-	0.2	-	mΩ
r_g	Internal gate resistance	Per switch		-	0	-	Ω

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions		Limits			Unit
				Min.	Typ.	Max.	
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)		-	-	193	K/kW
$R_{th(j-c)D}$		Junction to case, per Inverter FWD (Note4)		-	-	304	
$R_{th(c-s)}$	Contact thermal resistance	Case to heat sink, per 1 module,	Thermal grease applied (Note4, 6)	-	36.6	-	K/kW
			PC-TIM applied (Note4, 7)	-	9.7	-	

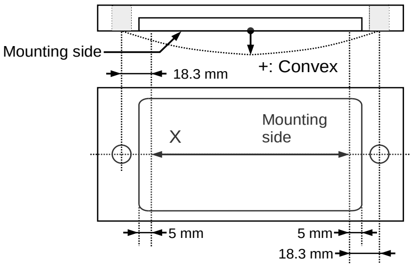
MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
M_t	Mounting torque	Main terminals M 5 screw	2.5	3.0	3.5	N·m
M_s	Mounting torque	Mounting to heat sink M 6 screw	3.5	4.0	4.5	N·m
d_s	Creepage distance	Terminal to terminal	18.4	-	-	mm
		Terminal to base plate	21.1	-	-	
d_a	Clearance	Terminal to terminal	9.6	-	-	mm
		Terminal to base plate	16.7	-	-	
e_c	Flatness of base plate	On the centerline (Note8)	±0	-	+200	μm
m	mass	-	-	120	-	g

*: This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

2. Junction temperature (T_{vj}) should not increase beyond T_{vjmax} rating.
3. Pulse width and repetition rate should be such that the device junction temperature (T_{vj}) dose not exceed T_{vjmax} rating.
4. Case temperature (T_C) and heat sink temperature (T_S) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.
6. Typical value is measured by using thermally conductive grease of λ=0.9 W/(m·K)/$D_{(C-S)}$=50 μm.
7. Typical value is measured by using PC-TIM of λ=3.4 W/(m·K)/$D_{(C-S)}$=50 μm.
8. The base plate (mounting side) flatness measurement point is as follows of the following figure.

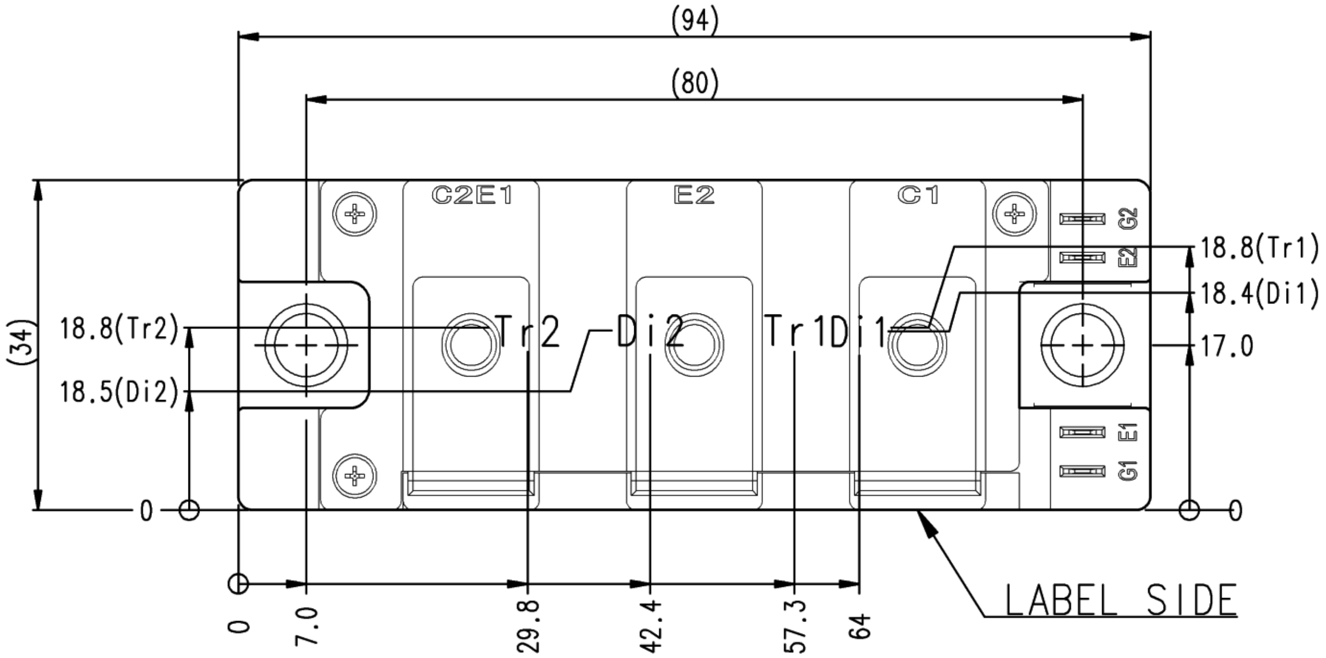


RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
V_{CC}	(DC) Supply voltage	Applied across C1-E2 terminals	-	300	450	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R_G	External gate resistance	Per switch	6.2	-	62	Ω

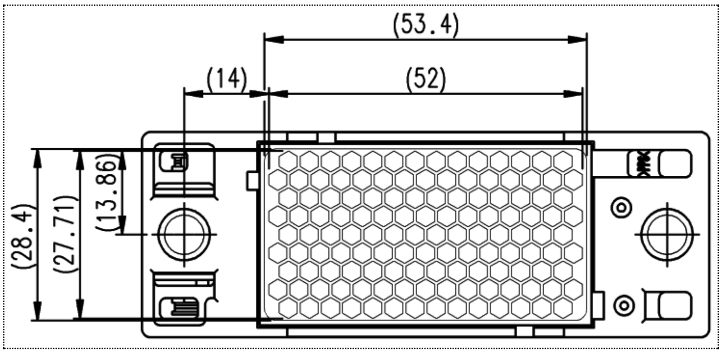
CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm

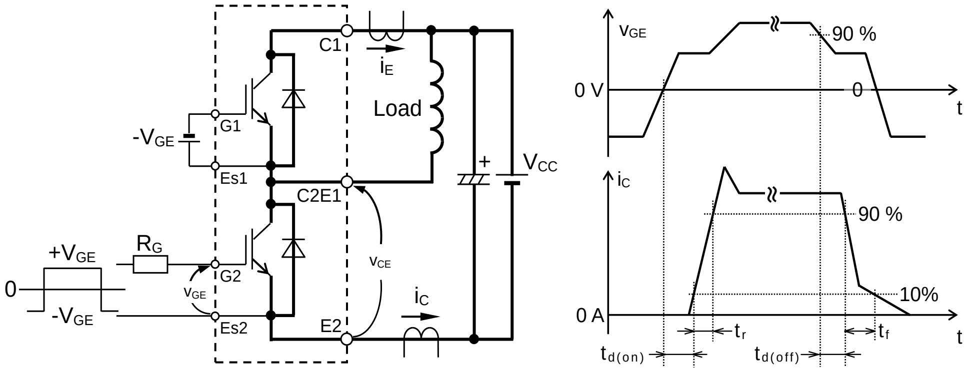


Tr1/Tr2: IGBT, Di1/Di2: FWD

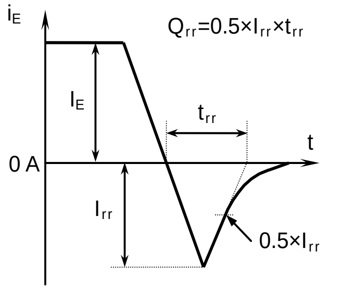
Option: PC-TIM applied baseplate outline



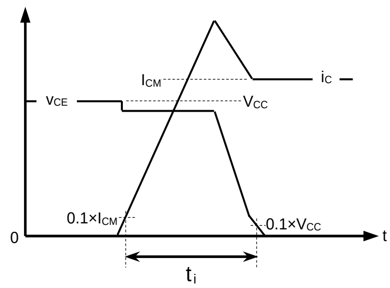
TEST CIRCUIT AND WAVEFORMS



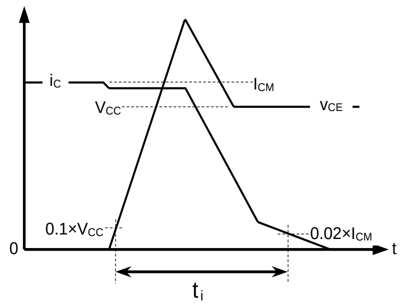
Switching characteristics test circuit and waveforms



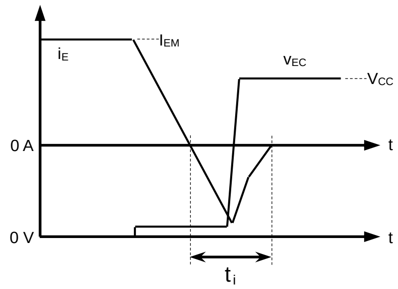
t_{rr}, Q_{rr} characteristics test waveform



IGBT Turn-on switching energy



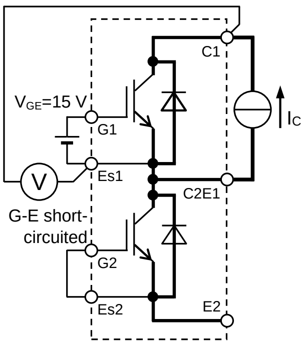
IGBT Turn-off switching energy



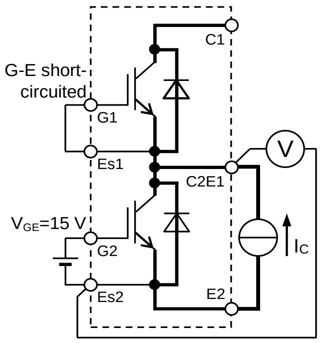
FWD Reverse recovery energy

Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

TEST CIRCUIT

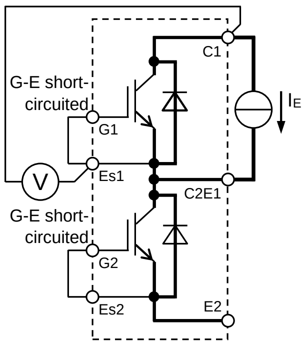


Tr1

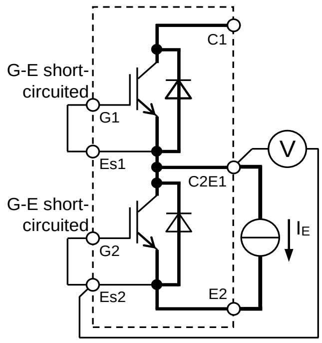


Tr2

V_{CEsat} characteristics test circuit



Di1

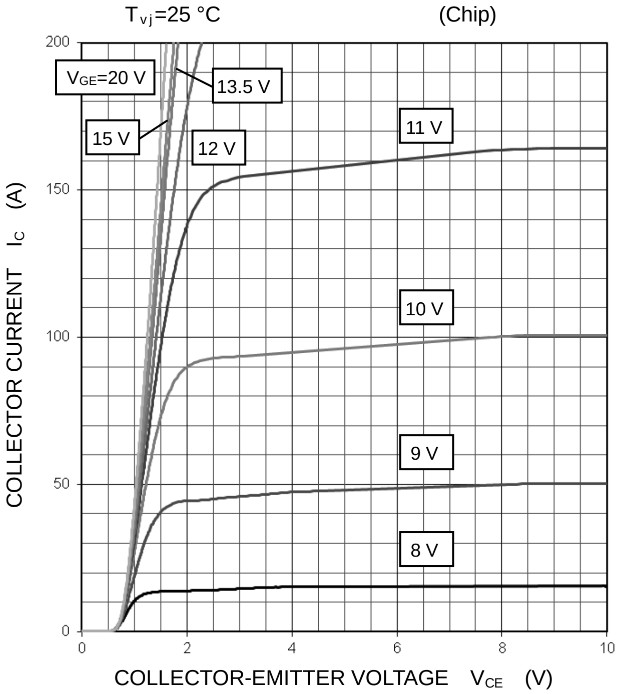


Di2

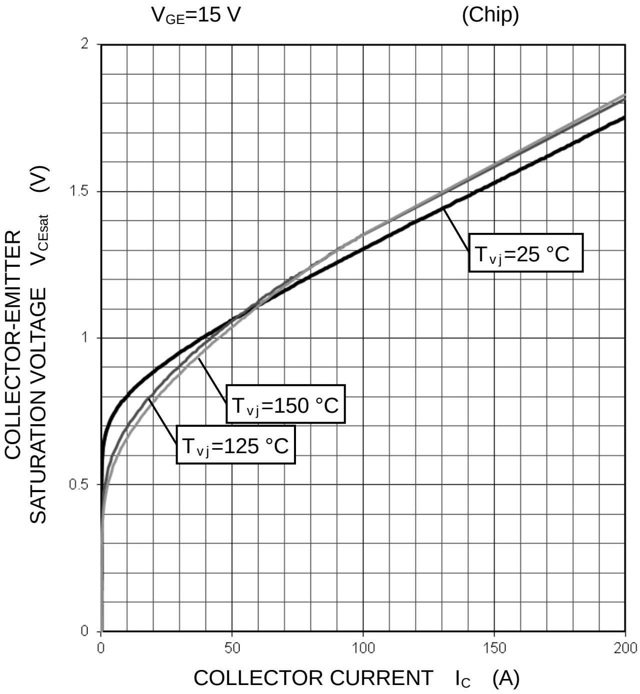
V_{EC} characteristics test circuit

PERFORMANCE CURVES

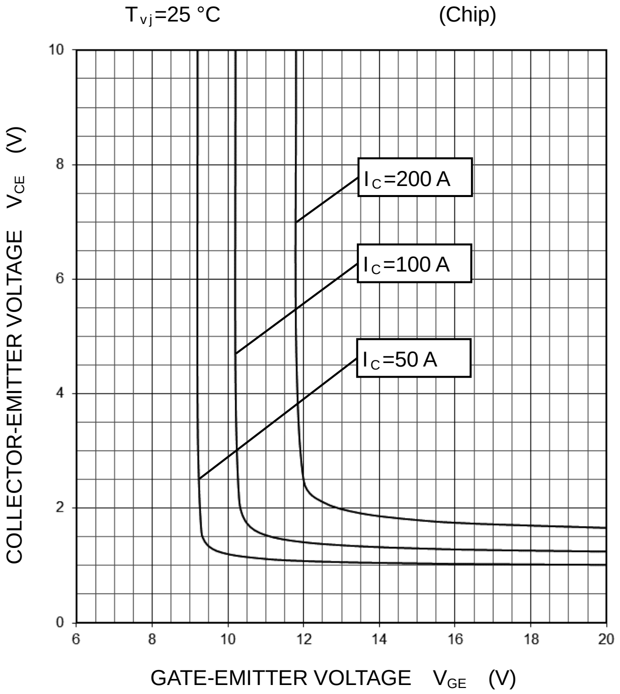
OUTPUT CHARACTERISTICS (TYPICAL)



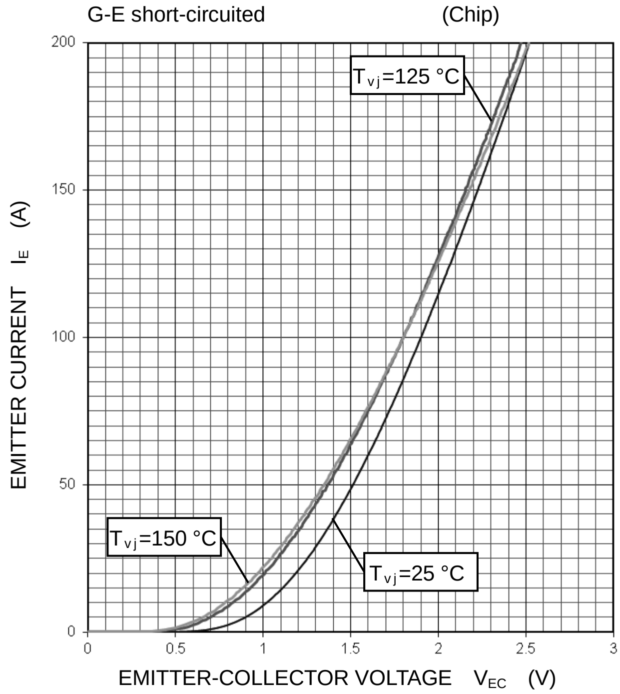
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)



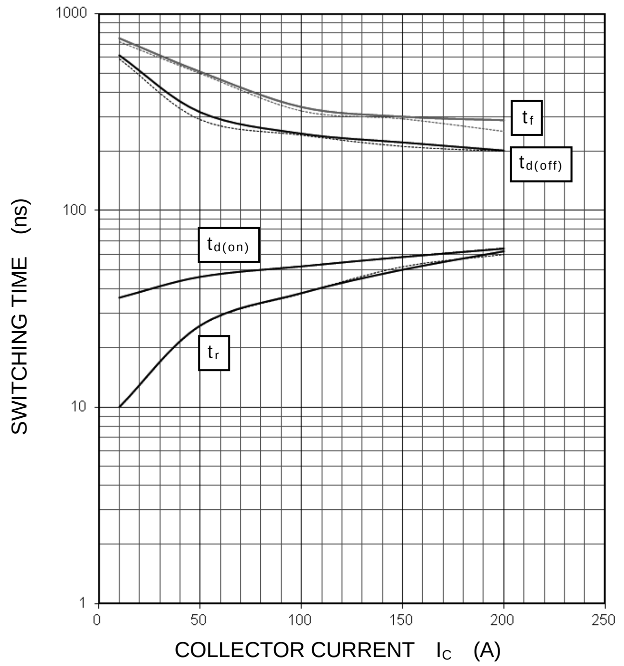
FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



PERFORMANCE CURVES

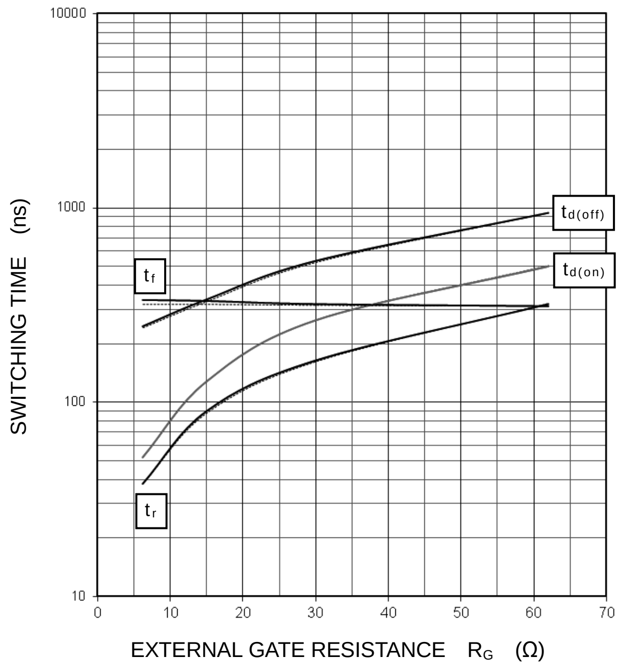
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=300 V, V_{GE}=±15 V, R_G=6.2 Ω, INDUCTIVE LOAD
———: T_{vj}=150 °C, - - - - -: T_{vj}=125 °C



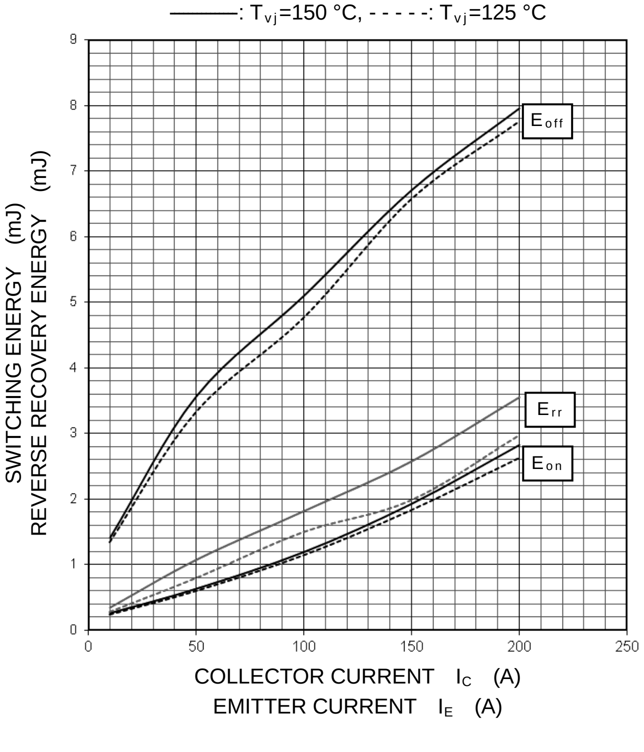
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=300 V, V_{GE}=±15 V, I_C=100 A, INDUCTIVE LOAD
———: T_{vj}=150 °C, - - - - -: T_{vj}=125 °C



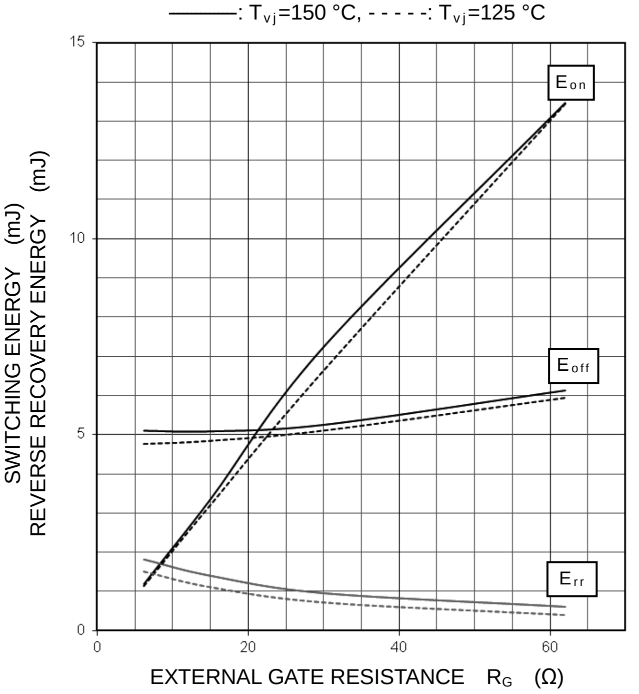
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=300 V, V_{GE}=±15 V, R_G=6.2 Ω,
INDUCTIVE LOAD, PER PULSE
———: T_{vj}=150 °C, - - - - -: T_{vj}=125 °C



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

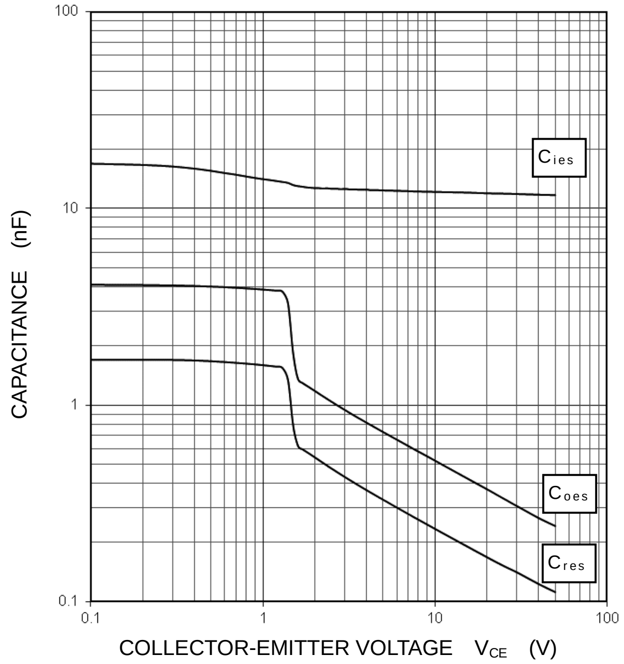
V_{CC}=300 V, V_{GE}=±15 V, I_C/I_E=100 A,
INDUCTIVE LOAD, PER PULSE
———: T_{vj}=150 °C, - - - - -: T_{vj}=125 °C



PERFORMANCE CURVES

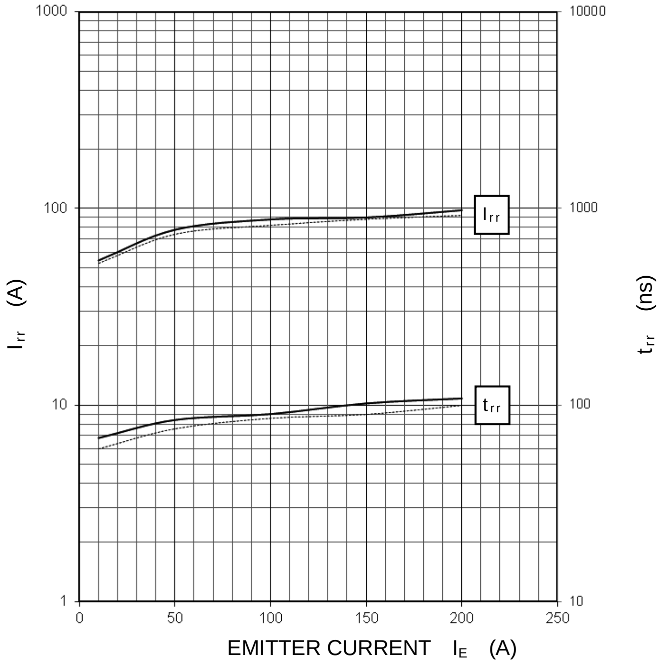
CAPACITANCE CHARACTERISTICS (TYPICAL)

G-E short-circuited, T_{vj}=25 °C



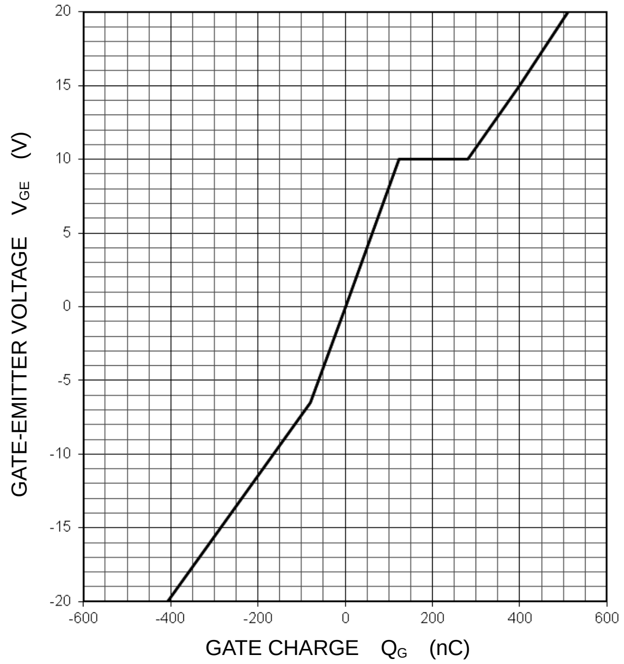
FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)

V_{CC}=300 V, V_{GE}=±15 V, R_G=6.2 Ω, INDUCTIVE LOAD
———: T_{vj}=150 °C, - - - - -: T_{vj}=125 °C



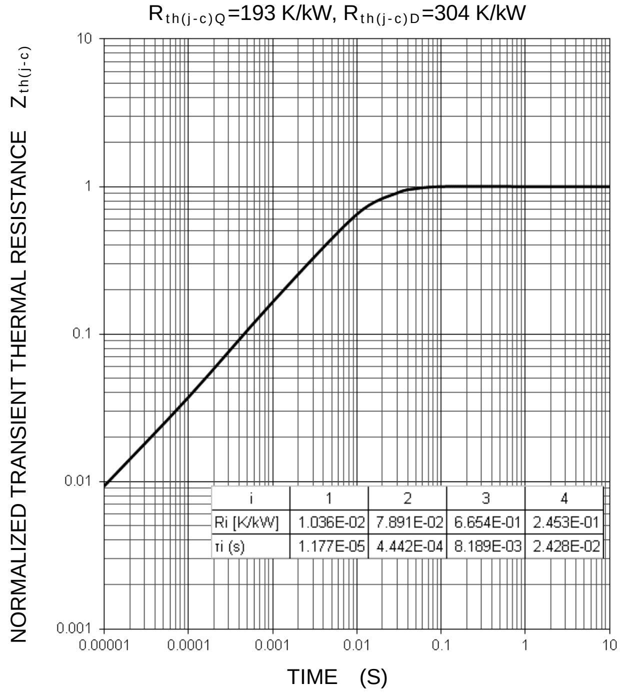
GATE CHARGE CHARACTERISTICS (TYPICAL)

V_{CC}=300 V, I_C=100 A, T_{vj}=25 °C



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

Single pulse, T_C=25 °C
$R_{th(j-c)Q}$=193 K/kW, $R_{th(j-c)D}$=304 K/kW



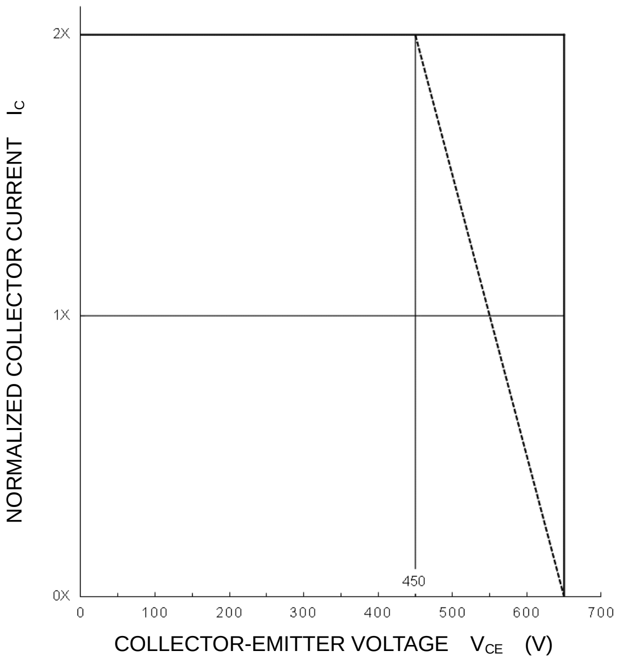
i	1	2	3	4
Ri [K/kW]	1.036E-02	7.891E-02	6.654E-01	2.453E-01
τi (s)	1.177E-05	4.442E-04	8.189E-03	2.428E-02

Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

PERFORMANCE CURVES

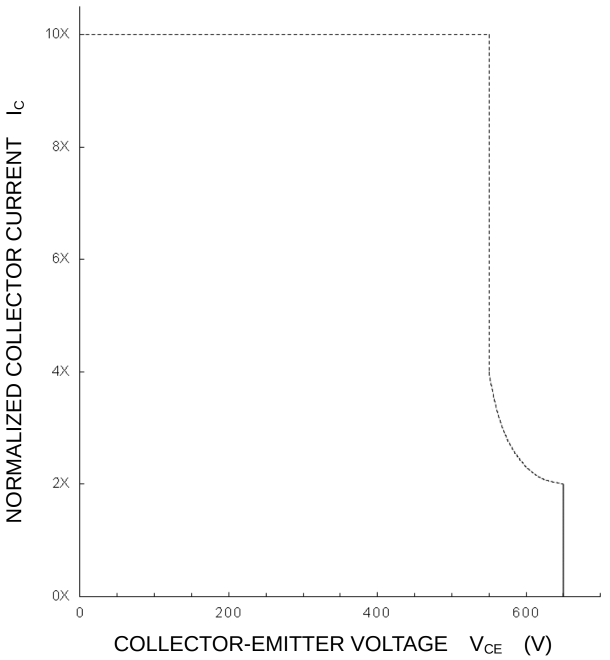
TURN-OFF SWITCHING SAFE OPERATING AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)

V_{CC}≤450 V, V_{GE}=±15 V, R_G=6.2~62 Ω,
———: T_{vj}=25~150 °C (Normal load operations (Continuous)
- - - - - -: T_{vj}=175 °C (Unusual load operations (Limited period)



SHORT-CIRCUIT SAFE OPERATING AREA (MAXIMUM)

V_{CC}≤400 V, V_{GE}=±15 V, R_G=6.2~62 Ω,
T_{vj}= 25 ~ 150 °C, t_W≤8 μs, Non-Repetitive



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