

<Intelligent Power Modules>

PM150CG1B065

FLAT-BASE TYPE INSULATED PACKAGE



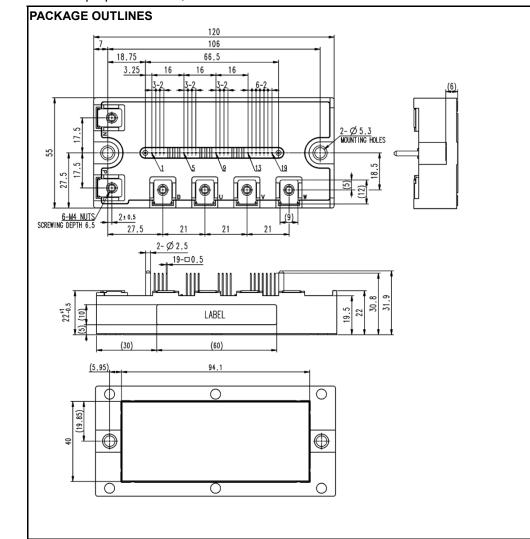
FEATURE

- a) Adopting Full-Gate CSTBT chip.
- b) The over-temperature protection which detects the chip surface temperature of CSTBT is adopted.
- c) Error output signal is available from each protection upper and lower arm of IPM.
- d) Outputting an error signal corresponding to the abnormal state (error mode identification)

This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.

APPLICATION

General purpose inverter, servo drives and other motor controls



Dimensions in mm

Tolerance otherwise specified

		-
Divisi Dimer	Tolerance	
0.5	to 3	±0.2
over 3	to 6	±0.3
over 6	to 30	±0.5
over 30	to 120	±0.8
over 120	to 400	±1.2

TERMINAL CODE

1.V_{UPC}

 $2.U_{FO}$

3.U_P

4.V_{UP1} 5.V_{VPC}

6.V_{FO}

6.V_{FO}

8.V_{VP1}

 $9.V_{WPC}$

10.W_{FO}

10.VVFO

12.V_{WP1}

 $13.V_{\text{NC}}$

 $14.V_{N1}$

15.NC

 $16.U_{N} \\$

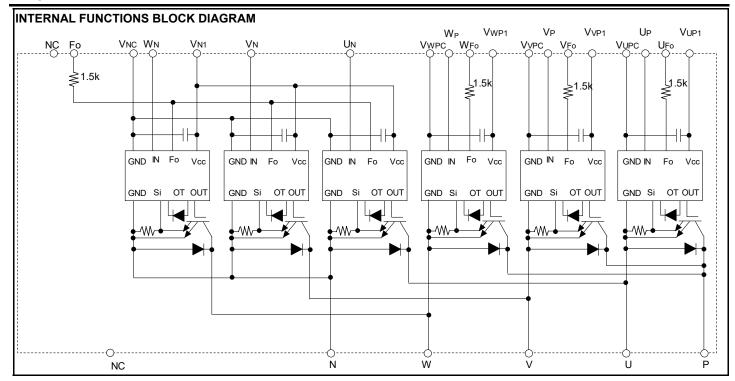
17.V_N 18.W_N

19.Fo

Publication date: Nov, 2017

HIGH POWER SWITCHING USE

INSULATED TYPE



MAXIMUM RATINGS (Tvj = 25°C, unless otherwise noted)

INVERTER PART

Symbol	Parameter	Conditions	Ratings	Unit
V _{CES}	Collector-Emitter Voltage	V _D =15 V, V _{CIN} =15 V	650	V
I _C	Callantan Commant	T _C =25 °C	150	_
I _{CRM}	Collector Current	Pulse	300	Α
P _{tot}	Total Power Dissipation	T _C =25 °C	480	W
l _E	Emitter Current	T _C =25 °C	150	_
I _{ERM}	(Free-wheeling Diode Forward current)	Pulse	300	Α
Tvj	Junction Temperature		-20 ~ +150	°C

^{*:} To measurement point is just under the chip.

CONTROL PART

OOMINO	E I AIXI			
Symbol	Parameter	Conditions	Ratings	Unit
V_D	Supply Voltage	Applied between: V _{UP1} -V _{UPC} , V _{VP1} -V _{VPC} , V _{WP1} -V _{WPC} , V _{N1} -V _{NC}	20	V
V_{CIN}	Input Voltage	Applied between: U _P -V _{UPC} , V _P -V _{VPC} , W _P -V _{WPC} , U _N , V _N , W _N -V _{NC}	20	V
V_{FO}	Fault Output Supply Voltage	Applied between: U _{FO} -V _{UPC} , V _{FO} -V _{VPC} , W _{FO} -V _{WPC} , Fo-V _{NC}	20	V
I _{FO}	Fault Output Current	Sink current at U _{FO} , V _{FO} , W _{FO} , Fo terminals	20	mA

TOTAL SYSTEM

Symbol	Parameter	Conditions	Ratings	Unit
$V_{CC(PROT)}$	Supply Voltage Protected by SC	V _D =13.5 V~16.5 V, Inverter Part, Tvj=+125°C start	400	V
T _{stg}	Storage Temperature	-	-40 ~ +125	°C
Tc	Operating Case Temperature	-	-20 ~ +125	°C
V _{isol}	Isolation Voltage	60Hz, Sinusoidal, Charged part to Base plate, AC 1min, RMS	2500	V

^{*:} Tc measurement point is just under the chip.

HIGH POWER SWITCHING USE

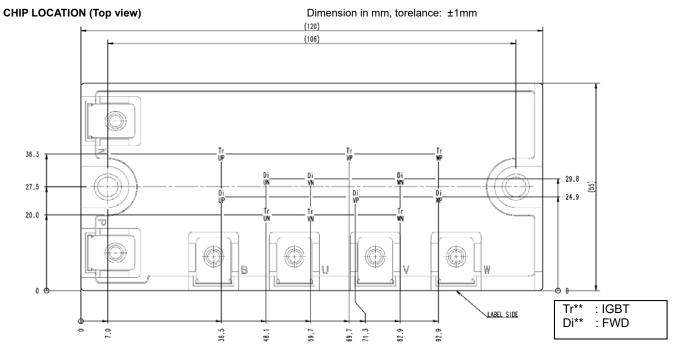
INSULATED TYPE

THERMAL RESISTANCE

Symbol Parameter	Doromator	Conditions	Limits			Unit
	Conditions	Min.	Тур.	Max.	Offic	
$R_{th(j-c)Q}$	Thermal Resistance	Junction to case, IGBT, per 1 element (Note1)	-	-	0.26	K/W
$R_{th(j-c)D}$		Junction to case, FWD, per 1 element (Note1)	-	-	0.42	IV/VV
R _{th(c-s)}	Contact Thermal Resistance	Case to heat sink, per 1 module,		14.4	_	K/kW
		Thermal grease applied (Note.1, 2)	-	14.4		IVIKVV

Note1. If you use this value, $R_{\text{th(s-a)}}$ should be measured just under the chips.

Note2. Typical value is measured by using thermally conductive grease of λ =0.9W/(m·K), D_(C-S)=50 μ m.



ELECTRICAL CHARACTERISTICS (Tvj= 25°C, unless otherwise noted)

INVERTER PART

Symbol	Parameter	Condition	Conditions			Limits		Unit
Symbol	Farameter	Conditions			Min.	Тур.	Max.	Offic
		V -45 V L -450A	Tvj=25 °C	Terminal	-	-	1.9	
		$V_D = 15 \text{ V, } I_C = 150 \text{A}$	1 Vj-25 C	Chip	-	1.25	-	V
V _{CEsat}	Collector-Emitter Saturation Voltage	V _{CIN} =0 V, Pulsed, (Fig.1)	Tvj=125 °C	Terminal	1	-	2.1	v
		V _{CIN} -0 V, Fulsed, (Fig. I)	1 Vj = 125 C	Chip	1	1.33	ı	
	V _{EC} Emitter-Collector Voltage	$V_D=15 \text{ V}, I_E=150 \text{A},$ $Tvj=25 ^{\circ}\text{C}$	Terminal	1	-	2.0		
\/			1 Vj-25 C	Chip	ı	1.40	ı	V
V EC		V _{CIN} = 15 V, pulsed, (Fig.2) Tvj=125 °C	Tvi=125 °C	Terminal	-	-	2.1	
			Chip	-	1.45	-		
t _{on}		V _D =15 V, V _{CIN} =0 V←→15 V,		0.30	0.80	1.20		
t _{rr}		V _{CC} =300 V, I _C =150A,		-	0.27	0.65		
t _{c(on)}	Switching Time	Tvj=125 °C,			-	0.24	0.75	μs
t _{off}		Inductive Load			-	0.82	2.30	
t _{c(off)}		(Fig.3, 4)			-	0.13	0.40	
	Collector Emitter Cut off Current	V _{CE} =V _{CES} , V _D =15 V,		Tvj=25 °C	-	-	1	A
ICES			-Oil Current III. A.	Tvj=125 °C	ı	-	10	mA

HIGH POWER SWITCHING USE

INSULATED TYPE

ELECTRICAL CHARACTERISTICS (Tvj = 25°C, unless otherwise noted)

CONTROL PART

Cymphal	Darameter	Parameter Conditions			Limits		Unit
Symbol	Parameter	Conditions	Conditions		Typ. N	Max.	Offic
		V -45 V V -45 V	V _{P1} -V _{PC}	-	4	6	
	Circuit Comment	V _D =15 V, V _{CIN} =15 V	V _{N1} -V _{NC}	-	12	18	
I _D	Circuit Current	V _D =15 V, V _{CIN} =0 V ←→15 V, V _{CC} =400 V	V _{P1} -V _{PC}	-	20	24	mA
		I _C =0A, Tvj=125 °C, f _C ≤20kHz	V _{N1} -V _{NC}	-	60	72	
$V_{th(ON)}$	Input ON Threshold Voltage	Applied between:	•	1.2	1.5	1.8	.,
$V_{th(OFF)}$	Input OFF Threshold Voltage	U_P - V_{UPC} , V_P - V_{VPC} , W_P - V_{WPC} , U_N , V_N , W_N - V_{NC}		1.7	2.0	2.3	V
SC	Short Circuit Trip Level	-20≤Tvj≤125 °C, V _D =15 V (Fig.3, 6)		300	-	-	Α
t _{d(SC)}	Short Circuit Current Delay Time	V _D =15 V, Tvj=125 °C (Fig.3, 6)		-	2.0	-	μs
ОТ		Detect to see a set of ICDT abis overfeed	Trip level	150	-	-	°C
OT _(hys)	Over Temperature Protection	Detect temperature of IGBT chip surface	Hysteresis	-	20	-	·
UV _t	Supply Circuit		Trip level	11.0	12.0	12.7	V
UV _r	Under-Voltage Protection	-	Reset level	-	12.5	-	V
I _{FO(H)}		V -45 V V -45 V (N-4-2)	•	-	-	0.01	^
I _{FO(L)}	Fault Output Current	V _D =15 V, V _{FO} =15 V (Note3)		-	10	15	mA
			ОТ	-	8.0	-	
t _{FO}	Fault Output Pulse Width	V _D =15 V (Note3)	UV	-	4.0	-	ms
			SC	-	2.0	-	

Note3. Fault output is given only when the internal SC, OT & UV protections schemes of either upper or lower arm device operate to protect it.

MECHANICAL RATINGS AND CHARACTERISTICS

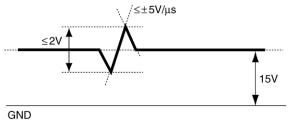
Symbol	Parameter	Conditions	Limits			Unit
Syllibol	yrribot Parameter Conditions		Min.	Тур.	Max.	Offic
Ms	Mounting Torque	Mounting part screw : M5	2.5	3.0	3.5	N•m
M_{t}	Mounting Torque	Main terminal part screw : M4	1.5	1.7	2.0	INTIII
m	mass	-	-	260	1	g

RECOMMENDED CONDITIONS FOR USE

Symbol	Parameter	Conditions	Recommended value	Unit
V _{CC}	Supply Voltage	Applied across P-N terminals	≤ 400	V
V _D	Control Supply Voltage	Applied between : V _{UP1} -V _{UPC} , V _{VP1} -V _{VPC} , V _{WP1} -V _{WPC} , V _{N1} -V _{NC} (Note4)	15.0±1.5	V
V _{CIN(ON)}	Input ON Voltage	Applied between :	≤ 0.8	\/
$V_{CIN(OFF)}$	Input OFF Voltage	$U_{P}\text{-}V_{UPC},V_{P}\text{-}V_{VPC},W_{P}\text{-}V_{WPC},U_{N},V_{N},W_{N}\text{-}V_{NC}$	≥ 9.0	V
f _{PWM}	PWM Input Frequency	Using Application Circuit of Fig. 8	≤ 20	kHz
t _{dead}	Arm Shoot-through Blocking Time	For IPM's each input signals (Fig.7)	≥ 2.0	μs

This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.

Note4. With ripple satisfying the following conditions: dv/dt swing ≤ ±5 V/µs, Variation ≤ 2 V peak to peak



PRECAUTIONS FOR TESTING

- 1. Before applying any control supply voltage (V_D), the input terminals should be pulled up by resistors, etc. to their corresponding supply voltage and each input signal should be kept off state.
 - After this, the specified ON and OFF level setting for each input signal should be done.
- 2. When performing "SC" tests, the turn-off surge voltage spike at the corresponding protection operation should not be allowed to rise above V_{CES} rating of the device.

(These test should not be done by using a curve tracer or its equivalent.)

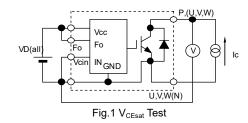
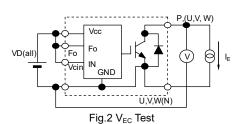


Fig.3 Switching time and SC test circuit



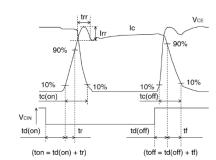


Fig.4 Switching time test waveform

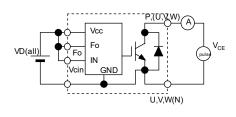


Fig.5 I_{CES} Test

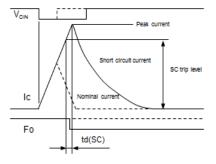
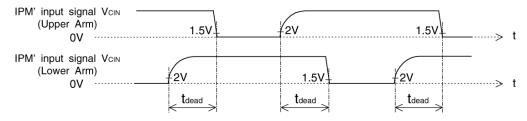


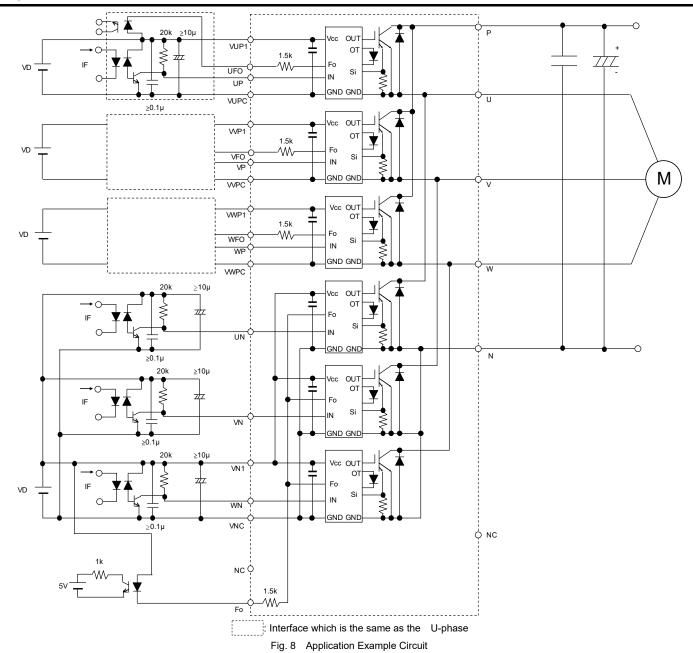
Fig.6 SC test waveform



1.5V: Input on threshold voltage Vth(on) typical value, 2V: Input off threshold voltage Vth(off) typical value

Fig. 7 Dead time measurement point example

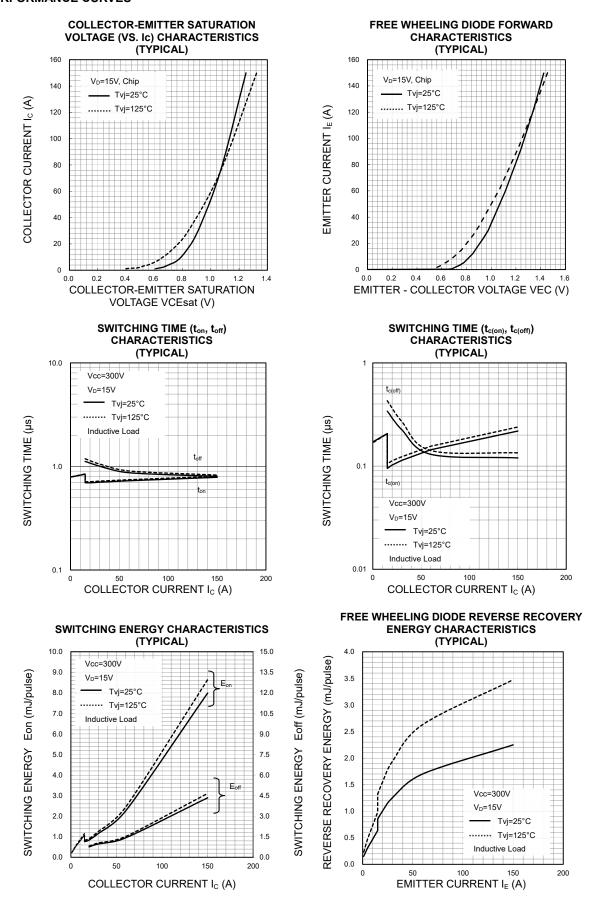
INSULATED TYPE

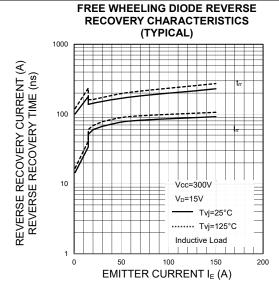


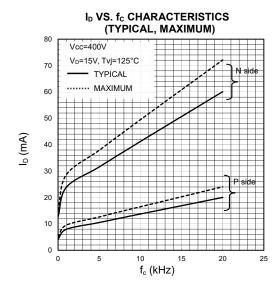
NOTES FOR STABLE AND SAFE OPERATION;

- Design the PCB pattern to minimize wiring length between opto-coupler and IPM's input terminal, and also to minimize the stray capacity between the input and output wirings of opto-coupler.
- Connect low impedance capacitor between the Vcc and GND terminal of each fast switching opto-coupler.
- Fast switching opto-couplers: t_{PLH} , $t_{PHL} \le 0.8 \mu s$, Use High CMR type.
- Slow switching opto-coupler: CTR > 100%
- Use 4 isolated control power supplies (V_D). Also, care should be taken to minimize the instantaneous voltage charge of the power supply.
- Make inductance of DC bus line as small as possible, and minimize surge voltage using snubber capacitor between P and N terminal.

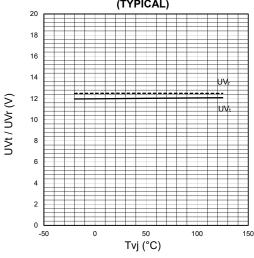
PERFORMANCE CURVES



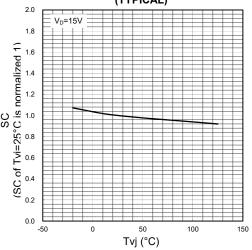




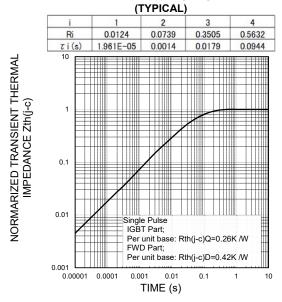








TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS



HIGH POWER SWITCHING USE INSULATED TYPE

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