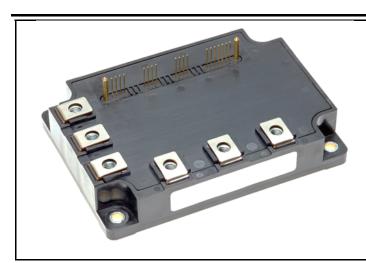


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PM300RG1C065

FLAT-BASE TYPE INSULATED PACKAGE



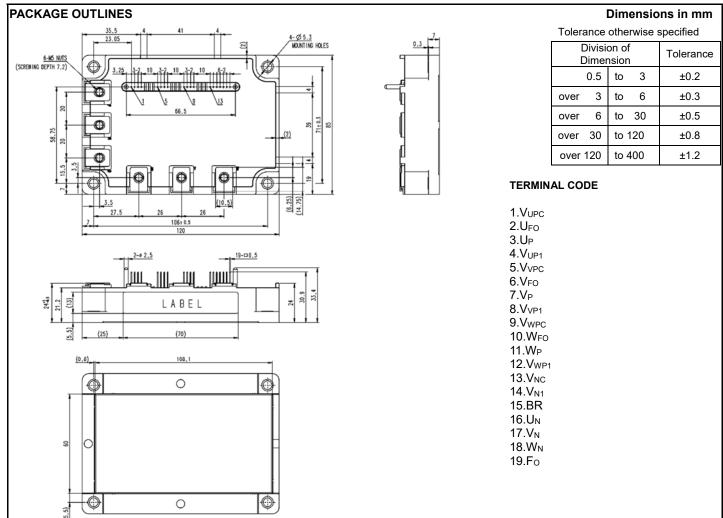
FEATURE

- a) Adopting Full-Gate CSTBT chip.
- b) The over-temperature protection which detects the chip surface temperature of CSTBT is adopted.
- c) Error output signal is available from each protection upper and lower arm of IPM.
- d) Outputting an error signal corresponding to the abnormal state (error mode identification)

This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.

APPLICATION

General purpose inverter, servo drives and other motor controls



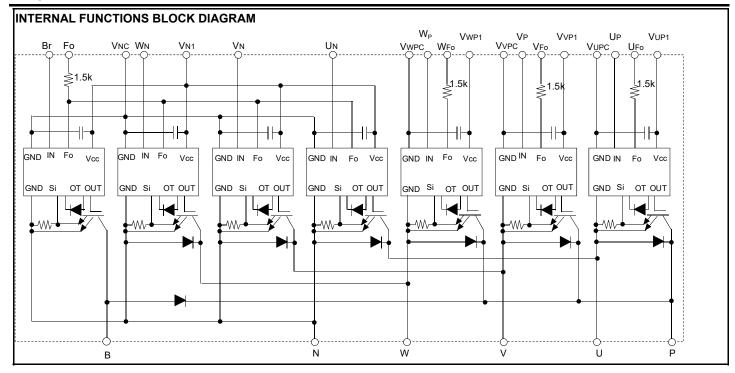
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Publication date: Nov, 2017

PM300RG1C065

HIGH POWER SWITCHING USE

INSULATED TYPE



MAXIMUM RATINGS (Tvj = 25°C, unless otherwise noted)

INVERTER PART

$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$		it i zati			
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	Symbol	Parameter	Conditions	Ratings	Unit
I _{CRM} Collector Current Pulse 600 P _{tot} Total Power Dissipation T _C =25 °C 1041 I _E Emitter Current T _C =25 °C 300 I _{ERM} (Free-wheeling Diode Forward current) Pulse 600	V_{CES}	Collector-Emitter Voltage	V _D =15 V, V _{CIN} =15 V	650	V
I _{CRM} Pulse 600 P _{tot} Total Power Dissipation T _C =25 °C 1041 I _E Emitter Current T _C =25 °C 300 I _{ERM} (Free-wheeling Diode Forward current) Pulse 600	Ic	Collector Current	T _C =25 °C	300	۸
I _E Emitter Current T _c =25 °C 300 I _{ERM} (Free-wheeling Diode Forward current) Pulse 600	I _{CRM}	-Collector Current	Pulse	600	- A
I _{ERM} (Free-wheeling Diode Forward current) Pulse 600	P_{tot}	Total Power Dissipation	T _C =25 °C	1041	W
	l _E	Emitter Current	T _C =25 °C	300	^
Tvj Junction Temperature -20 ~ +150	I _{ERM}	(Free-wheeling Diode Forward current)	Pulse	600	A
	Tvj	Junction Temperature		-20 ~ +150	°C

^{*:} Tc measurement point is just under the chip.

BRAKE PART

Symbol	Parameter	Conditions	Ratings	Unit
V _{CES}	Collector-Emitter Voltage	V _D =15 V, V _{CIN} =15 V	650	V
I _C	Collector Current	T _C =25 °C	150	^
I _{CRM}	Collector Current	Pulse	300	Α
P _{tot}	Total Power Dissipation	T _C =25 °C	595	W
V _{R(DC)}	Diode Rated Reverse DC Voltage	T _C =25 °C	650	V
I _F	Diode Forward Current	T _C =25 °C	150	Α
Tvj	Junction Temperature		-20 ~ +150	°C

 $[\]ensuremath{^{*:}}$ Tc measurement point is just under the chip.

CONTROL PART

Symbol	Parameter	Conditions	Ratings	Unit
V_D	Supply Voltage	Applied between: V _{UP1} -V _{UPC} , V _{VP1} -V _{VPC} , V _{WP1} -V _{WPC} , V _{N1} -V _{NC}	20	V
V_{CIN}	Input Voltage	Applied between: U_P - V_{UPC} , V_P - V_{VPC} , W_P - V_{WPC} , U_N , V_N , W_N , Br - V_{NC}	20	V
V_{FO}	Fault Output Supply Voltage	Applied between: U _{FO} -V _{UPC} , V _{FO} -V _{VPC} , W _{FO} -V _{WPC} , Fo-V _{NC}	20	V
I _{FO}	Fault Output Current	Sink current at U _{FO} , V _{FO} , W _{FO} , Fo terminals	20	mA

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HIGH POWER SWITCHING USE INSULATED TYPE

TOTAL SYSTEM

Symbol	Parameter	Conditions	Ratings	Unit
V _{CC(PROT)}	Supply Voltage Protected by SC	V _D =13.5 V∼16.5 V, Inverter Part, Tvj=+125°C start	400	V
T_{stg}	Storage Temperature	-	-40 ~ +125	°C
Tc	Operating Case Temperature	-	-20 ~ +125	°C
V _{isol}	Isolation Voltage	60Hz, Sinusoidal, Charged part to Base plate, AC 1min, RMS	2500	V

^{*:} Tc measurement point is just under the chip.

THERMAL RESISTANCE

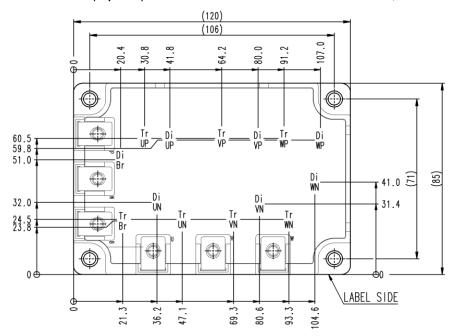
Symbol	Parameter	Candidana		1.1		
		Conditions	Min.	Тур.	Max.	Unit
$R_{th(j-c)Q}$	Thermal Resistance	Inverter, Junction to case, IGBT, per 1 element (Note1)	-	-	0.12	
R _{th(j-c)D}		Inverter, Junction to case, FWD, per 1 element (Note1)	-	-	0.19	K/W
$R_{th(j-c)Q}$		Brake, Junction to case, IGBT, per 1 element (Note1)	-	-	0.21	
$R_{th(j-c)D}$		Brake, Junction to case, FWD, per 1 element (Note1)	-	-	0.34	
R _{th(c-s)}	Contact Thermal Resistance	Case to heat sink, per 1 module,	- 8.4	_	K/kW	
		Thermal grease applied (Note.1, 2)		0.4	_ 	IVIXVV

Note1. If you use this value, $R_{\text{th(s-a)}}\,\text{should}$ be measured just under the chips.

Note2. Typical value is measured by using thermally conductive grease of λ =0.9W/(m·K), $D_{\text{(C-S)}}$ =50 μ m.

CHIP LOCATION (Top view)

Dimension in mm, torelance: ±1mm



Tr** : IGBT Di** : FWD

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PM300RG1C065

HIGH POWER SWITCHING USE

INSULATED TYPE

ELECTRICAL CHARACTERISTICS (Tvj= 25°C, unless otherwise noted)

INVERTER PART

Cumbal	Parameter	Conditions		Limits			Unit	
Symbol	Parameter	Conditions			Min.	Тур.	Max.	Unit
		V -45 V I -200 A	T:-25 °C	Terminal	-	-	2.05	
V	Collector-Emitter Saturation Voltage	V _D =15 V, I _C =300 A	Tvj=25 °C	Chip	-	1.25	-	V
V _{CEsat}	· ·	V _{CIN} =0 V, Pulsed, (Fig.1)	Tvj=125 °C	Terminal	-	-	2.3	V
		V _{CIN} -0 V, Fuisea, (Fig. 1)	1 Vj=125 C	Chip	1	1.33	-	
V _{EC} E		V _D =15 V, I _E =300A,	Tvj=25 °C	Terminal	-	-	2.1	V
				Chip	ı	1.40	-	
		V _{CIN} = 15 V, pulsed, (Fig.2) Tvj=125 °C	Tvi=125 °C	Terminal	ı	ı	2.2	
			Chip	ı	1.45	ı		
t _{on}		$V_D = 15 \text{ V}, V_{CIN} = 0 \text{ V} \longleftrightarrow 15 \text{ V},$ $V_{CC} = 300 \text{ V}, I_C = 300 \text{ A},$			0.3	0.6	1.2	
t _{rr}					-	0.2	0.65	
$t_{c(on)}$	Switching Time	Tvj=125 °C,			-	0.2	0.75	μs
t _{off}		Inductive Load		-	1.1	2.3		
$t_{c(off)}$		(Fig.3, 4)			-	0.16	0.4	
	Collector-Emitter Cut-off Current	V _{CE} =V _{CES} , V _D =15 V, V _{CIN} =15 V (Fig.5)		Tvj=25 °C	-	-	1	mΛ
I _{CES}				Tvj=125 °C	-	-	10	mA

BRAKE PART

Cumbal	Parameter	Constituion o		Limits			I Imia	
Symbol	Farameter	Conditions			Min.	Тур.	Max.	Unit
		V _D =15 V, I _C =150A	Tvj=25 °C	Terminal	-	-	1.9	
		VB-13 V, IC-130A	1 0 20 0	Chip	-	1.25	1	.,
V _{CEsat}	Collector-Emitter Saturation Voltage	V _{CIN} =0 V, Pulsed, (Fig.1)	Tvj=125 °C	Terminal	-	-	2.1	V
				Chip	-	1.33	-	
	Diode Forward Voltage	I _F =150A Tvj=25 °C Tvj=125 °C	Tvj=25 °C	Terminal	-	-	2.0	- - V
V				Chip	-	1.40	-	
V_{FM}			T : 405.00	Terminal	-	-	2.1	
			Chip	-	1.45	-		
	Collector-Emitter Cut-off Current	V _{CE} =V _{CES} , V _D =15 V, V _{CIN} =15 V (Fig.5)		Tvj=25 °C	-	-	1	Л
I _{CES}				Tvj=125 °C	-	-	10	mA

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PM300RG1C065

HIGH POWER SWITCHING USE

INSULATED TYPE

ELECTRICAL CHARACTERISTICS (Tvj = 25°C, unless otherwise noted)

CONTROL PART

Cumahaal	Danama et en	Quantities and		Limits			1.1
Symbol	Parameter	Conditions	Conditions		Тур.	Max.	Unit
		V -45 V V -45 V	V _{P1} -V _{PC}	-	4	6	
	Cincuit Command	V _D =15 V, V _{CIN} =15 V	V _{N1} -V _{NC}	-	16	24	^
I _D	Circuit Current	V _D =15 V, V _{CIN} =0 V←15 V, V _{CC} =400 V	V _{P1} -V _{PC}	-	36	44	mA
		I _C =0A, Tvj=125 °C, f _C ≤20kHz	V _{N1} -V _{NC}	-	129	151	
$V_{th(ON)}$	Input ON Threshold Voltage	Applied between:		1.2	1.5	1.8	V
$V_{th(OFF)}$	Input OFF Threshold Voltage	$U_{P}\text{-}V_{UPC},V_{P}\text{-}V_{VPC},W_{P}\text{-}V_{WPC},U_{N},V_{N},W_{N},$	Br-V _{NC}	1.7	2.0	2.3	V
sc	Short Circuit Trip Level	-20≤Tvj≤125 °C, V _D =15 V (Fig.3, 6)	Inverter	600	-	-	^
			Brake	300	-	-	A
t _{d(SC)}	Short Circuit Current Delay Time	V _D =15 V, Tvj=125 °C (Fig.3, 6)	V _D =15 V, Tvj=125 °C (Fig.3, 6)		2.0	-	μs
ОТ	Out Tanana and the Dual attan	Detect to many engines of ICDT objections and	Trip level	150	-	-	°C
OT _(hys)	Over Temperature Protection	Detect temperature of IGBT chip surface	Hysteresis	-	20	-	
UV _t	Supply Circuit		Trip level	11.0	12.0	12.7	V
UV _r	Under-Voltage Protection	-	Reset level	-	12.5	-	V
I _{FO(H)}	Facility Contract Comment	V -45 V V -45 V (Note 2)		-	-	0.01	^
I _{FO(L)}	Fault Output Current	V _D =15 V, V _{FO} =15 V (Note3)		-	10	15	mA
			ОТ	-	8.0	-	
t _{FO}	Fault Output Pulse Width	V _D =15 V (Note3)	UV	-	4.0	-	ms
			sc	-	2.0	-	

Note3. Fault output is given only when the internal SC, OT & UV protections schemes of either upper or lower arm device operate to protect it.

MECHANICAL RATINGS AND CHARACTERISTICS

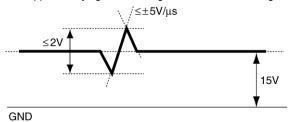
Symbol	Parameter	Conditions		Unit		
		Conditions		Тур.	Max.	Unit
Ms	Mounting Torque	Mounting part screw : M5	2.5	3.0	3.5	N•m
M_t	Mounting Torque	Main terminal part screw : M5	2.5	3.0	3.5	INTIII
m	mass	-	-	425	-	g

RECOMMENDED CONDITIONS FOR USE

Symbol	Parameter	Conditions	Recommended value	Unit
V _{CC}	Supply Voltage	Applied across P-N terminals	≤ 400	V
V _D	Control Supply Voltage	Applied between: V _{UP1} -V _{UPC} , V _{VP1} -V _{VPC} , V _{WP1} -V _{WPC} ,V _{N1} -V _{NC} (Note4)	15.0±1.5	V
V _{CIN(ON)}	Input ON Voltage	Applied between :	≤ 0.8	V
$V_{CIN(OFF)}$	Input OFF Voltage	U_P - V_{UPC} , V_P - V_{VPC} , W_P - V_{WPC} , U_N , V_N , W_N , Br - V_{NC}	≥ 9.0	v
f _{PWM}	PWM Input Frequency	Using Application Circuit of Fig. 8	≤ 20	kHz
t _{dead}	Arm Shoot-through Blocking Time	For IPM's each input signals (Fig.7)	≥ 2.0	μs

This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.

Note4. With ripple satisfying the following conditions: dv/dt swing ≤ ±5 V/µs, Variation ≤ 2 V peak to peak



INSULATED TYPE

PRECAUTIONS FOR TESTING

- 1. Before applying any control supply voltage (V_D), the input terminals should be pulled up by resistors, etc. to their corresponding supply voltage and each input signal should be kept off state.
 - After this, the specified ON and OFF level setting for each input signal should be done.
- 2. When performing "SC" tests, the turn-off surge voltage spike at the corresponding protection operation should not be allowed to rise above V_{CES} rating of the device.

(These test should not be done by using a curve tracer or its equivalent.)

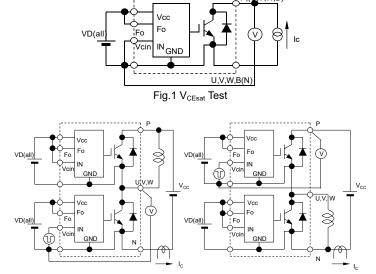
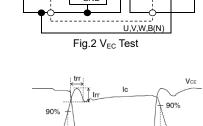


Fig.3 Switching time and SC test circuit



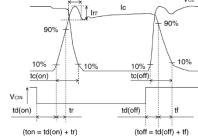


Fig.4 Switching time test waveform

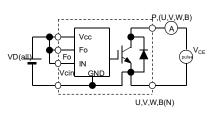


Fig.5 I_{CES} Test

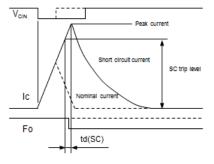
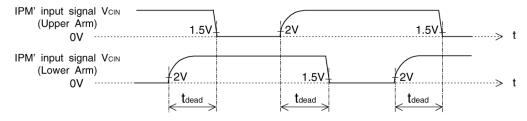


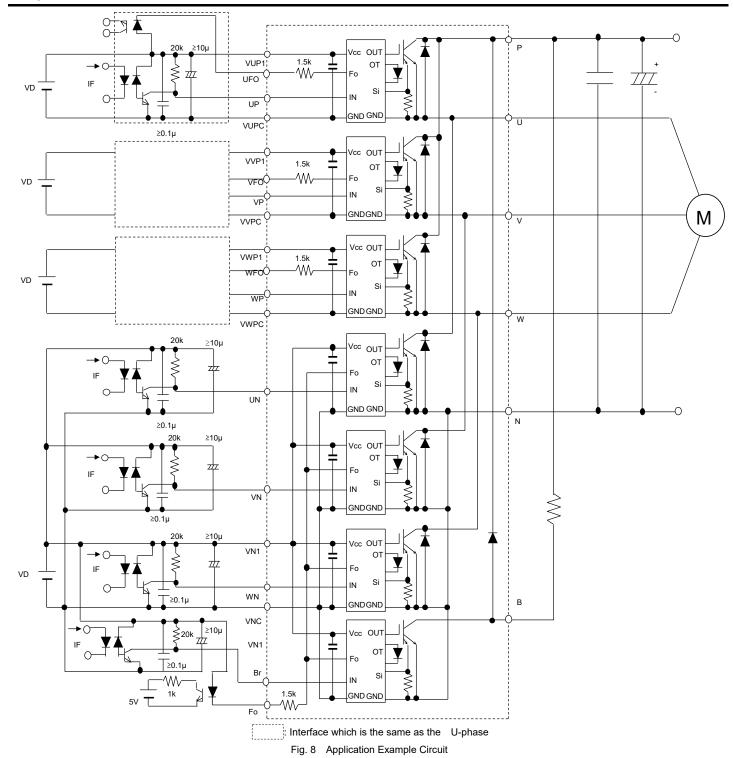
Fig.6 SC test waveform



1.5V: Input on threshold voltage Vth(on) typical value, 2V: Input off threshold voltage Vth(off) typical value

Fig. 7 Dead time measurement point example

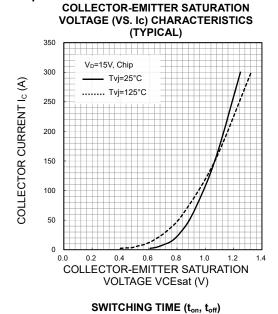
INSULATED TYPE

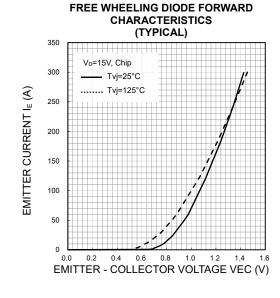


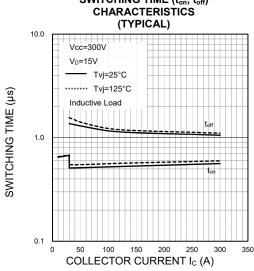
NOTES FOR STABLE AND SAFE OPERATION;

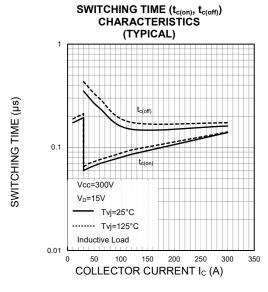
- Design the PCB pattern to minimize wiring length between opto-coupler and IPM's input terminal, and also to minimize the stray capacity between the input and output wirings of opto-coupler.
- · Connect low impedance capacitor between the Vcc and GND terminal of each fast switching opto-coupler.
- Fast switching opto-couplers: t_{PLH} , $t_{PHL} \le 0.8 \mu s$, Use High CMR type.
- Slow switching opto-coupler: CTR > 100% (*can be applied to Brake part input signal, in this case, resistor should be selected properly).
- Use 4 isolated control power supplies (V_D). Also, care should be taken to minimize the instantaneous voltage charge of the power supply.
- Make inductance of DC bus line as small as possible, and minimize surge voltage using snubber capacitor between P and N terminal.

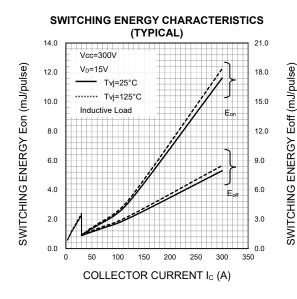
PERFORMANCE CURVES Inverter part

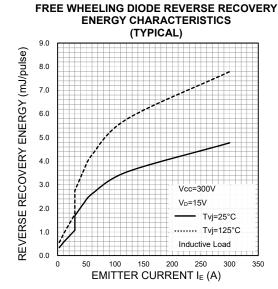


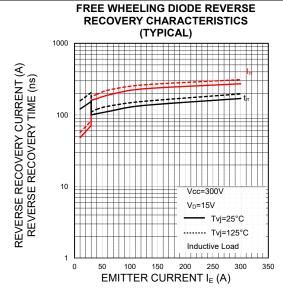


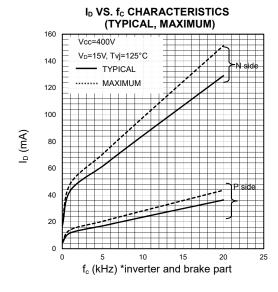




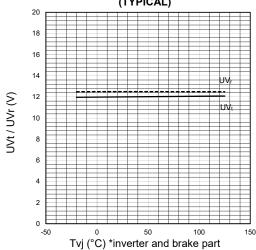




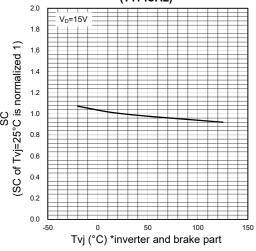




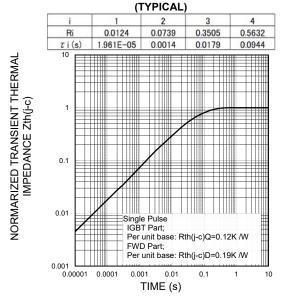
UV TRIP LEVEL VS. TVJ CHARACTERISTICS (TYPICAL)







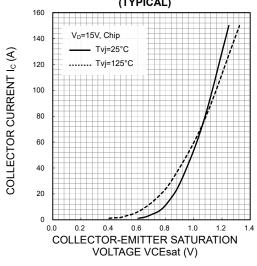
TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS



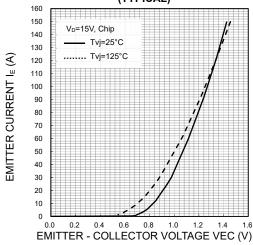
HIGH POWER SWITCHING USE INSULATED TYPE

PERFORMANCE CURVES Brake part

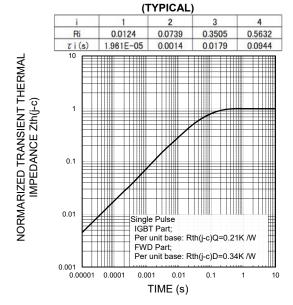
COLLECTOR-EMITTER SATURATION VOLTAGE (VS. Ic) CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS



PM300RG1C065

HIGH POWER SWITCHING USE INSULATED TYPE

Keep safety first in your circuit designs!

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